



PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application of

Hideki IWAKI *et al.*

Serial No. 09/994,683

: Art Unit: 2812

Filed: 28 November 2001

: Examiner: Leonardo ANDUTAR

For: MULTI-LEVEL CIRCUIT SUBSTRATE,  
METHOD FOR MANUFACTURING  
SAME AND METHOD FOR ADJUSTING  
CHARACTERISTIC IMPEDANCE  
THEREFOR

: Attorney Docket: P64916US1

5/Election  
JACOBSON  
6/6/03  
RECEIVED  
TECHNOLOGY CENTER 2800  
MAR 25 2003  
CENTER 2800

**RESPONSE TO RESTRICTION REQUIREMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Applicant in response to the Office Action dated 26 February 2003 (Paper No. 4), hereby elects Claim 1 through 11 for further prosecution in this application.

An early and favorable action is respectfully solicited.

Respectfully submitted,  
JACOBSON HOLMAN, PLLC

Date: 24 March 2003  
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